

CONNECTION TO HFC

ENTRANCE UNIT

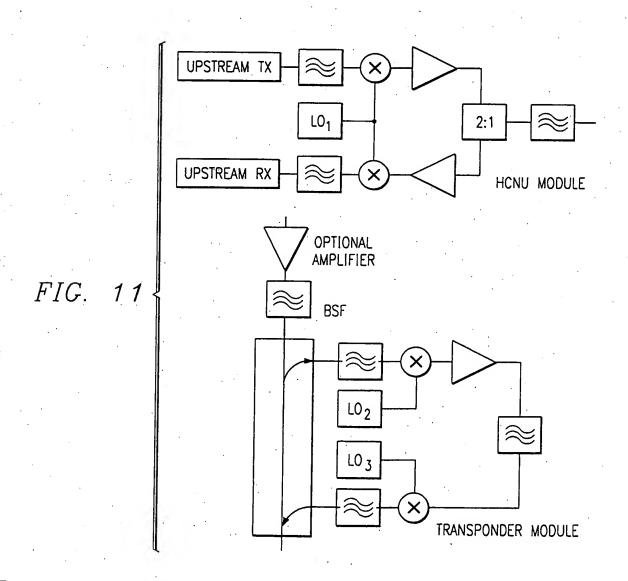
AMPLIFIER + NOTCH FILTER
+ SPLITTER + POWER FEEDER

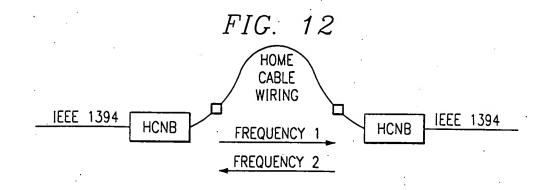
HCNU + BLUETOOTH

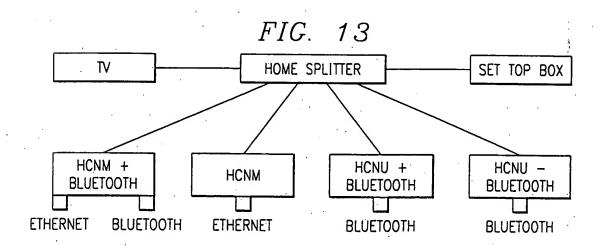
(WITHOUT POWER UNIT)

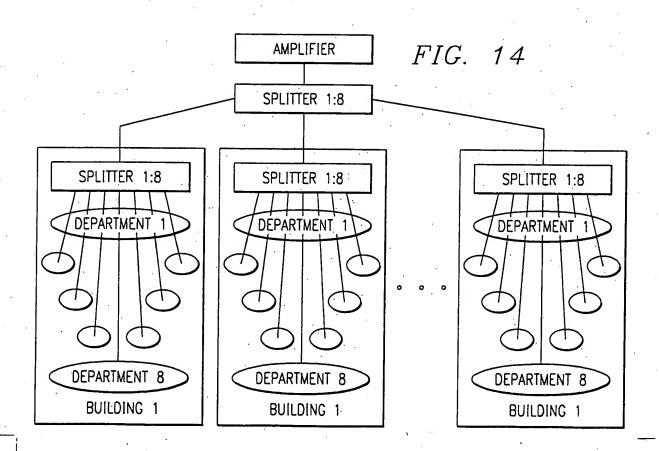
HCNU + BLUETOOTH

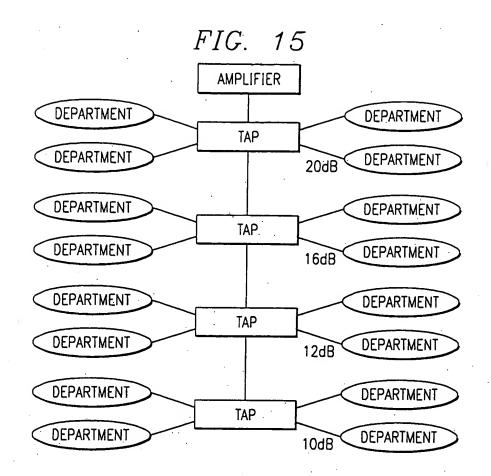
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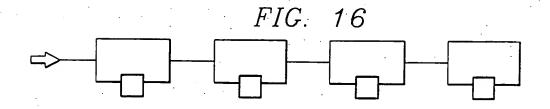












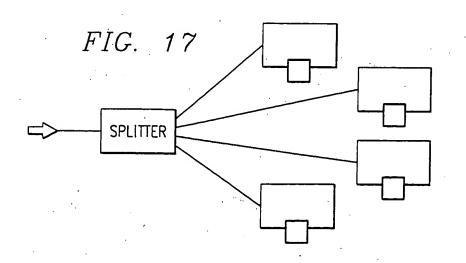


FIG. 18

ETHERNET

CPU 860 (OR ARM) DOCSIS 1.1 IEEE802.11MAC 4230 . CM MAC DOCSIS CHANNEL 4030 CM PHY

TUNER

HOME NETWORKING CHANNEL 4030

CM PHY

FPGA IEEE802.11MAC (HW PART) + GLUE LOGIC

4521 CMTS BURST RECEIVER 900MHz TX + RX FRONT END

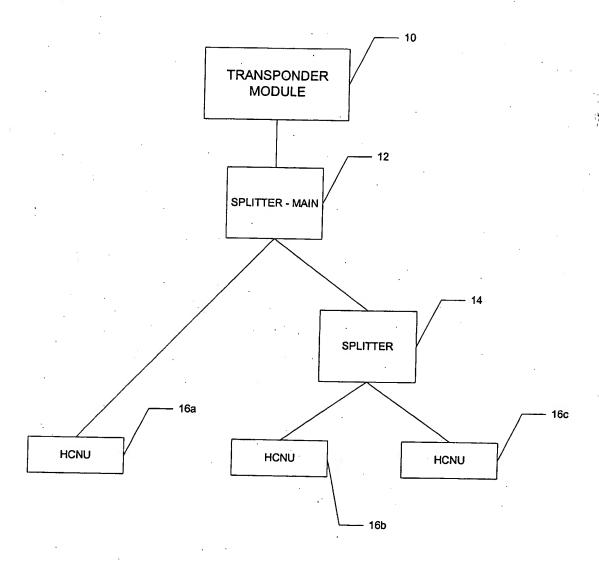
ETHERNET

CPU 860 (OR ARM) DOCSIS 1.1 IEEE802.11MAC FIG. 19

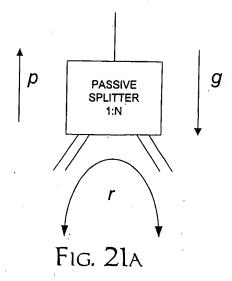
HOME NETWORKING CHANNEL
4030

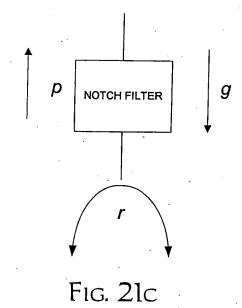
FPGA IEEE802.11MAC (HW PART) + GLUE LOGIC 4030 CM PHY

4521 CMTS BURST RECEIVER 900MHz TX + RX FRONT END



F1G. 20





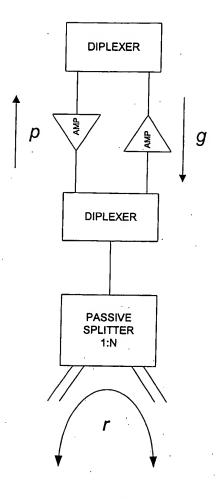


Fig. 21B